

1. General description

WSJT65R075DE is a high voltage N-channel MOSFET in ITO247 package, which utilizes the advanced super-junction technology to provide superior FOM $R_{DS(on)} \cdot Q_g$ among silicon based MOSFETs. It is particularly suitable for applications require extreme high efficiency and power density.



2. Features and benefits

- Superior FOM $R_{DS(on)} \cdot Q_g$
- Extremely low switching loss
- Integrated ultrafast body diode
- 100% avalanche tested

3. Applications

- EV charger
- High efficiency power supplies
- On board charger
- Inverters

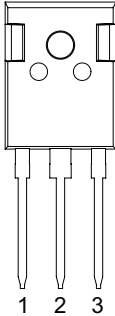
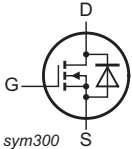
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute maximum rating							
V _{DS}	drain-source voltage			650			V
V _{GS}	gate-source voltage			±30			V
I _D	continuous drain current	T _C = 25 °C		30			A
P _{tot}	power dissipation	T _C = 25 °C		162			W
T _j	junction temperature			-55 to 150			°C
Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
R _{DS(on)}	drain-source on-state resistance	V _{GS} = 10 V, I _D = 25 A		-	55	70	mΩ
Dynamic characteristics							
Q _{G(tot)}	total gate charge	I _D = 25 A; V _{DS} = 400 V; V _{GS} = 10 V		-	70	-	nC
E _{OSS}	coss stored energy	V _{GS} = 0 V; V _{DS} = 0 to 400 V		-	10.7	-	μJ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain		
3	S	source		
mb	n.c.	mounting base; isolated		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WSJT65R075DE	IITO247	WSJT65R075DEQ	Tube	30	IITO247A	10-Nov-2025

7. Marking

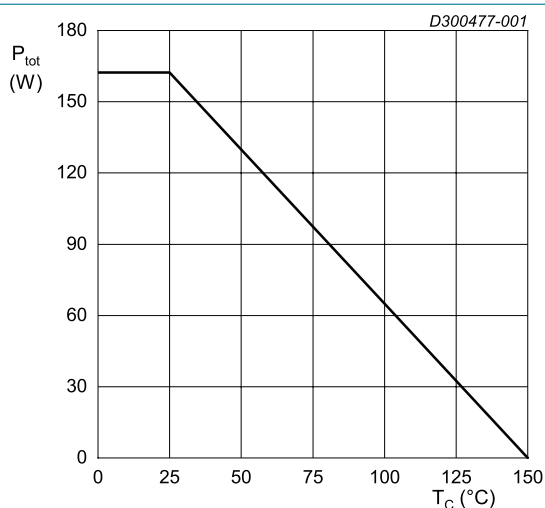
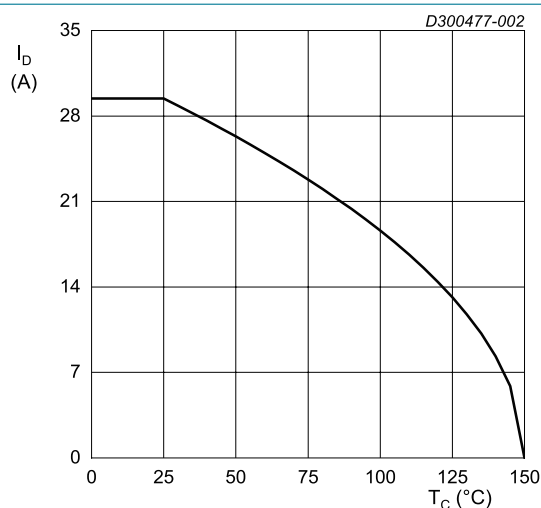
Table 4. Marking codes

Type number	Marking codes
WSJT65R075DE	WSJT 65R075DE

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

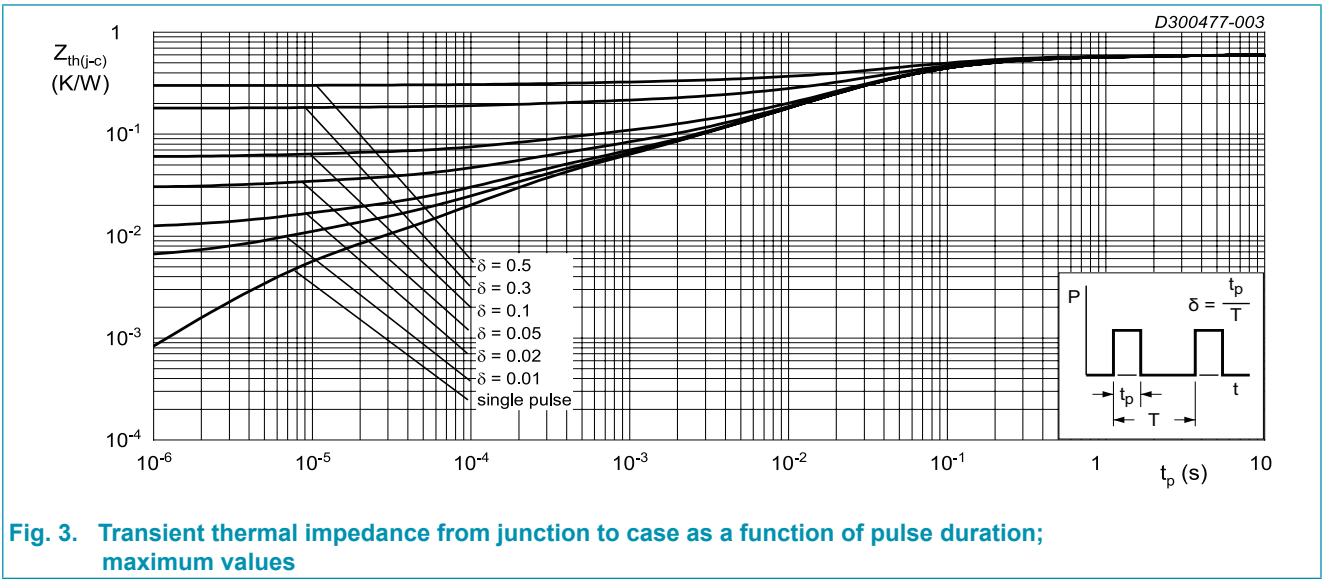
Symbol	Parameter	Conditions	Notes	Values	Unit
V_{DS}	drain-source voltage			650	V
V_{GS}	gate-source voltage			± 30	V
I_D	continuous drain current	$T_C = 25\text{ }^{\circ}\text{C}$		30	A
		$T_C = 100\text{ }^{\circ}\text{C}$		18	A
I_{DM}	pulsed drain current	$T_C = 25\text{ }^{\circ}\text{C}$		120	A
P_{tot}	power dissipation	$T_C = 25\text{ }^{\circ}\text{C}$		162	W
E_{AS}	single pulse drain-to-source avalanche	$I_{AS} = 10\text{ A}$; $R_{GS} = 25\text{ }\Omega$; $V_{DD} = 50\text{ V}$; $T_J = 25\text{ }^{\circ}\text{C}$		500	mJ
E_{AR}	repetitive avalanche energy	$I_{AS} = 10\text{ A}$; $R_{GS} = 25\text{ }\Omega$; $V_{DD} = 50\text{ V}$; $T_J = 25\text{ }^{\circ}\text{C}$		1.5	mJ
I_{AS}	avalanche current, single pulse			10	A
dv/dt	MOSFET dv/dt ruggedness			50	V/ns
dv/dt	reverse diode dv/dt			50	V/ns
dI _F /dt	maximum diode commutation speed			850	A/ μ s
T_{stg}	storage temperature			-55 to 150	$^{\circ}\text{C}$
T_J	junction temperature			-55 to 150	$^{\circ}\text{C}$


Fig. 1. Total power dissipation as a function of case temperature

Fig. 2. Continuous Drain Current as a function of case temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j-c)}$	thermal resistance from junction to case			-	0.50	0.77	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air		-	45	-	K/W



10. Isolation characteristics

Table 7. Isolation characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$V_{isol(RMS)}$	RMS isolation voltage	from all terminals to external heatsink; sinusoidal waveform; clean and dust free; 50 Hz ≤ f ≤ 60 Hz; T _h = 25 °C; RH ≤ 65 %		-	-	2500	V

11. Characteristics

Table 8. Characteristics

$T_j = 25\text{ }^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 1 mA; V _{GS} = 0 V		650	-	-	V
V _{GS(th)}	gate-source threshold voltage	I _D = 250 μA; V _{DS} = V _{GS}		3.0	-	5.0	V
I _{DSS}	drain leakage current	V _{DS} = 650 V; V _{GS} = 0 V		-	-	10	μA
		V _{DS} = 650 V; V _{GS} = 0 V; T _J = 125 °C		-	300	-	μA
I _{GSS}	gate leakage current	V _{GS} = ±30 V; V _{DS} = 0 V		-	-	±100	nA
R _{DS(on)}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A		-	55	70	mΩ
R _G	gate resistance	f = 1 MHz		-	1.0	-	Ω
Dynamic characteristics							
Q _{G(tot)}	total gate charge	I _D = 25 A; V _{DS} = 400 V; V _{GS} = 10 V		-	70	-	nC
Q _{GS}	gate-source charge			-	26	-	nC
Q _{GD}	gate-drain charge			-	19	-	nC
C _{iss}	input capacitance	V _{DS} = 400 V; V _{GS} = 0 V; f = 250 KHz		-	4241	-	pF
C _{oss}	output capacitance			-	84	-	pF
C _{rss}	reverse transfer capacitance			-	2.3	-	pF
C _{o(er)}	effective output capacitance, energy related	V _{GS} = 0 V; V _{DS} = 0 to 400 V		-	133	-	pF
C _{o(tr)}	effective output capacitance, time related			-	831	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = 400 V; V _{GS} = 10 V; R _G = 5.1 Ω; I _D = 25 A		-	65	-	ns
t _r	rise time			-	12	-	ns
t _{d(off)}	turn-off delay time			-	54	-	ns
t _f	fall time			-	2.1	-	ns
Source-drain diode							
V _{SD}	source-drain voltage	V _{GS} = 0 V; I _S = 25 A		-	0.87	1.2	V
I _S	body-diode continuous current	T _C = 25 °C		-	-	30	A
t _{rr}	reverse recovery time	V _R = 400 V; I _F = 25 A; dI _F /dt = 100 A/μs		-	180	-	ns
Q _{rr}	reverse recovered charge			-	1.6	-	μC
I _{rrm}	reverse recovery current			-	17	-	A

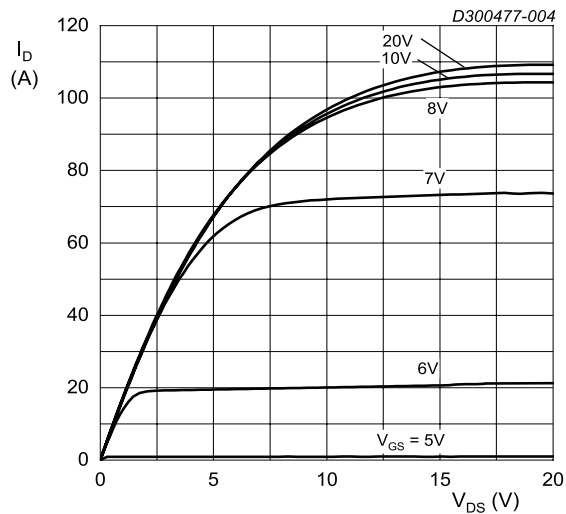


Fig. 4. Drain current as a function of drain-source voltage; typical values

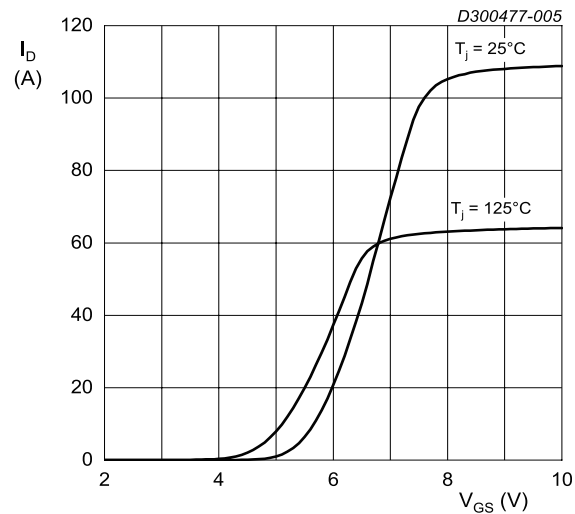


Fig. 5. Drain current as a function of gate-source voltage; typical values

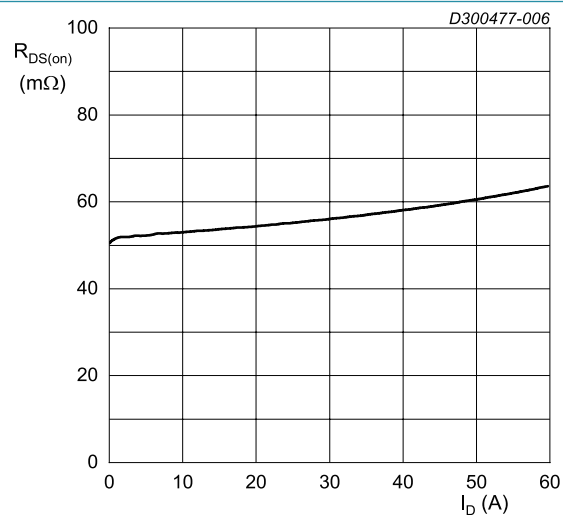


Fig. 6. Drain-source on-state resistance as a function of drain current; typical values

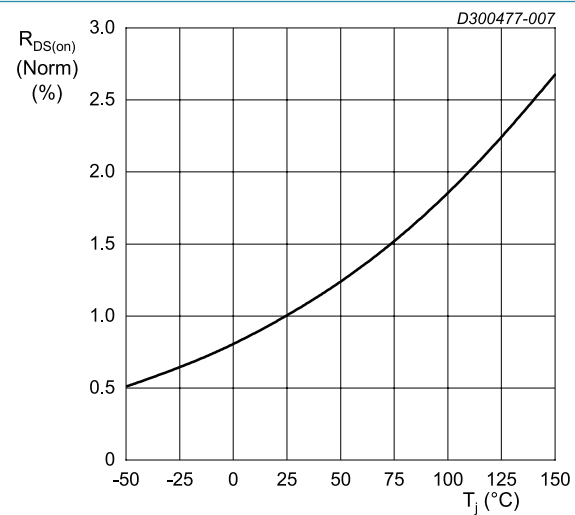
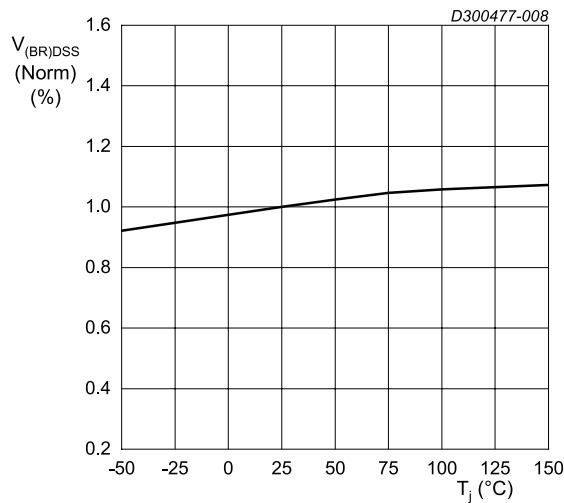
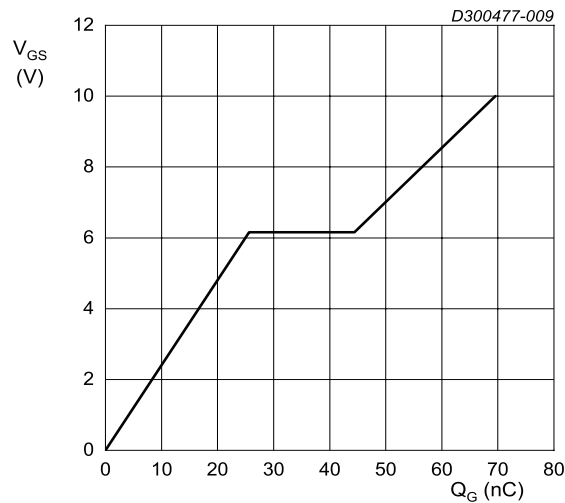


Fig. 7. Normalized drain-source on-state resistance as a function of junction temperature



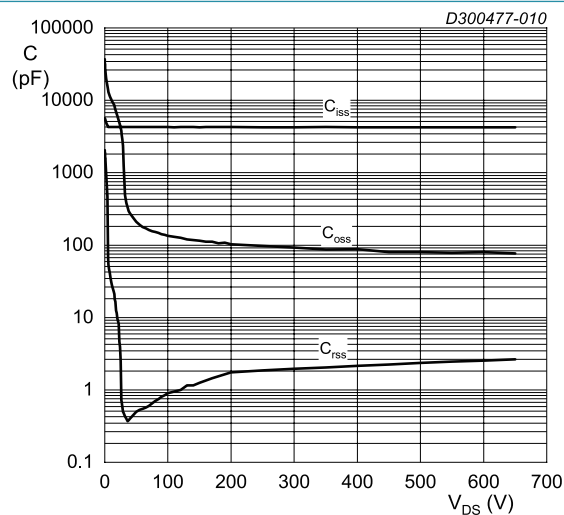
$I_D = 1 \text{ mA}$

Fig. 8. Normalized drain-source breakdown voltage as a function of junction temperature



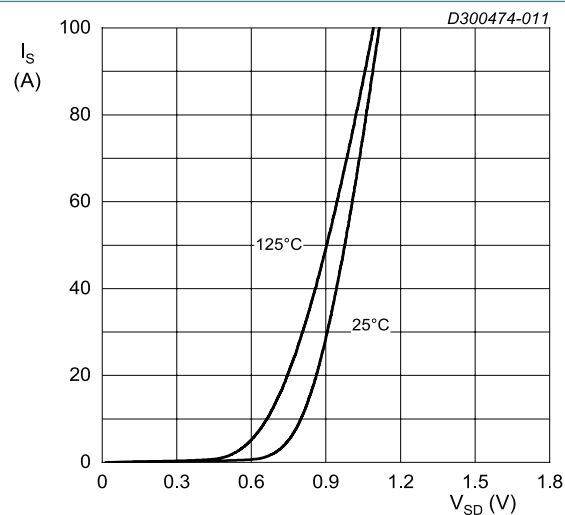
$I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}$

Fig. 9. Gate-source voltage as a function of gate charge; typical values



$V_{GS} = 0 \text{ V}; f = 250 \text{ KHz}$

Fig 10. Capacitances as a function of drain-source voltage; typical values



$V_{GS} = 0 \text{ V}$

Fig 11. Source current as a function of source-drain voltage; typical values

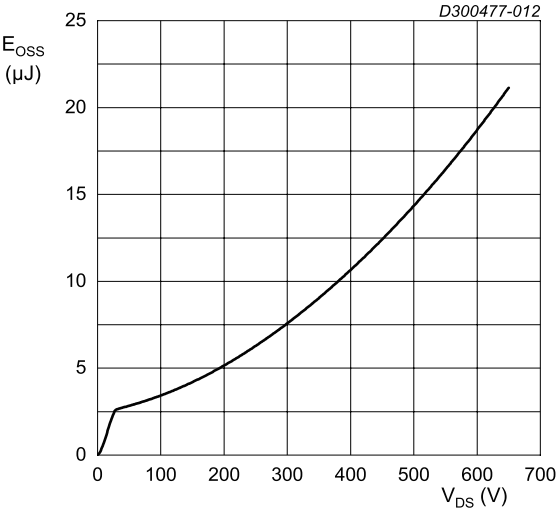
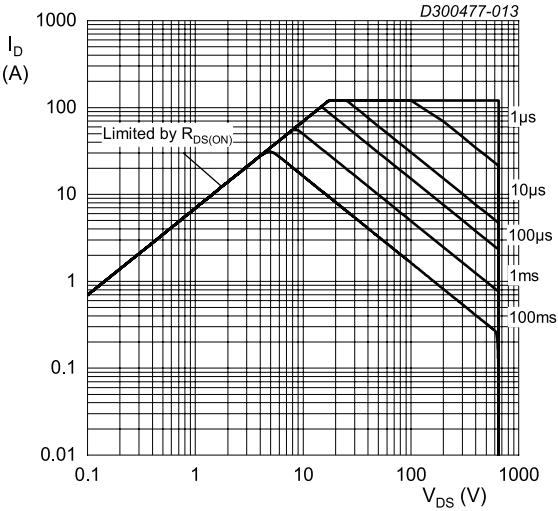


Fig. 12. Output capacitance stored energy as a function of drain-source voltage

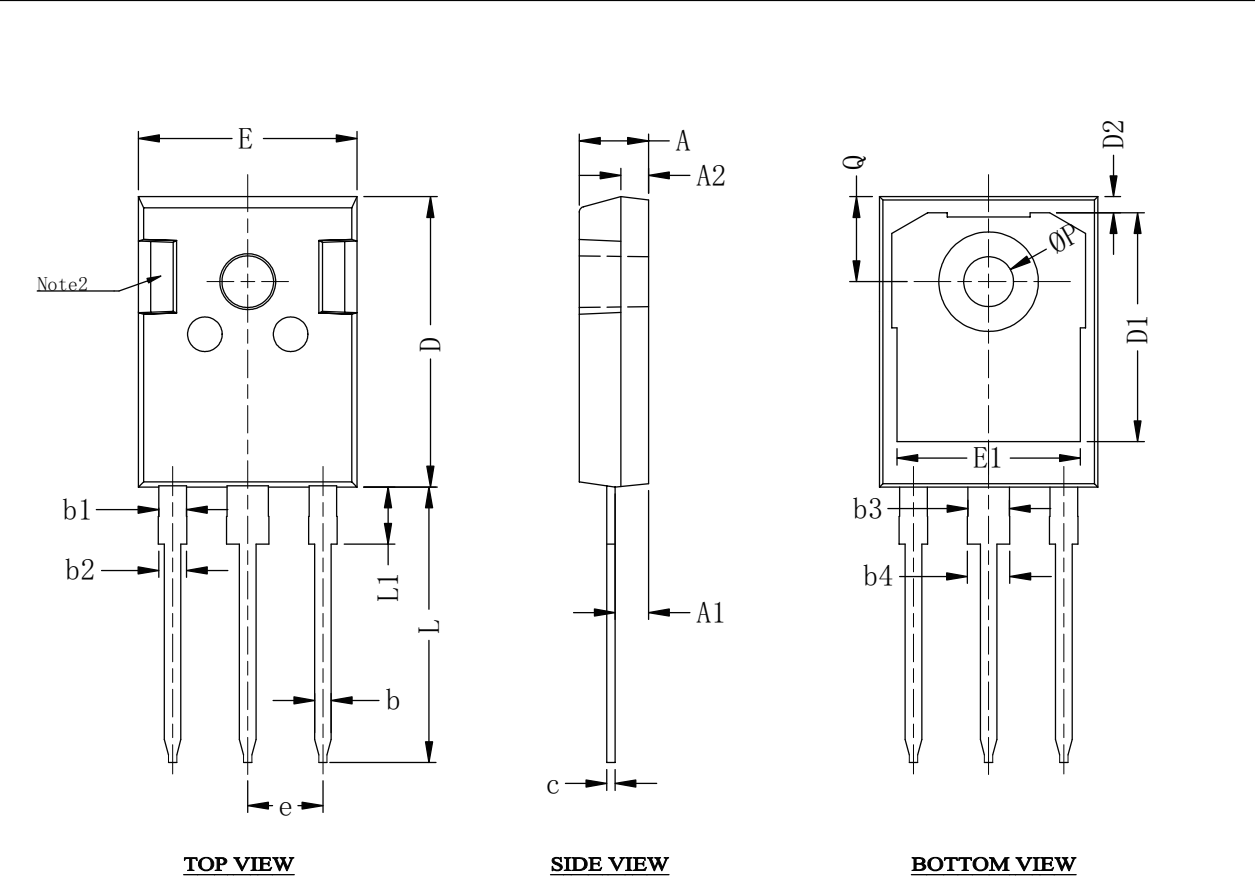


$T_C = 25\text{ }^{\circ}\text{C}$
Fig. 13. Safe operating area

12. Package outline

Plastic single-ended through-hole package; isolated heatsink mounted;1 mounting hole; 3 leads T0-247

IIT0247



UNIT	A	A1	A2	b	b1	b2	b3	b4	c	D	D1	D2	E	E1	e	L	L1	P	Q
mm	5.10	2.52	2.10	1.27	2.07	2.20	3.07	3.20	0.66	21.10	16.51	1.35	15.90	13.50	5.44	20.12	4.30	3.70	6.15
	4.90	2.31	1.90	1.14	1.94	2.00	2.94	3.00	0.57	20.90	15.91	1.05	15.70	13.06		19.72		3.55	

Note:
1. All dimensions do not include mold flash & gate remain
2. Both Sn finish and bare copper are acceptable.

13. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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