**Product data sheet** 

# 1. General description

WMS30N060V is a high performance logic level N-channel MOSFET in PDFN5X6 package, which utilizes advanced Trench MOSFET technology to provide low  $R_{\tiny DS(on)}$  and gate charge. It is designed and qualified in a wide range of industrial and consumer applications.





# 2. Features and benefits

- · Advance High Cell Density Trench Technology
- Low R<sub>DS(on)</sub> to Minimize Conduction Losses
- Low Capacitance to Minimize Switching Losses
- · Optimized Gate Charge to Minimize Driver Losses
- 100% UIS Tested
- · RoHS Compliant and Halogen Free

# 3. Applications

- DC-DC Converters
- BLDC Motor Control
- Load Switch
- Lithium-ion Battery Protection

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit		
Absolute	maximum rating		·						
$V_{DS}$	drain-source voltage				30		V		
$V_{GS}$	gate-source voltage				±20		V		
I <sub>D</sub>	continuous drain current	V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 25 °C	[1]	45			А		
P <sub>tot</sub>	power dissipation	T <sub>mb</sub> = 25 °C		42			W		
T <sub>j</sub>	junction temperature			-55 to 150			°C		
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit		
Static cha	aracteristics								
R <sub>DS(on)</sub>	drain-source on-state	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		-	4.5	6.0	mΩ		
	resistance	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		-	6.6	9.0	mΩ		
Dynamic o	Dynamic characteristics								
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 20 A; V <sub>DS</sub> = 15 V; V <sub>GS</sub> = 10 V		-	40	-	nC		

# 5. Pinning information

## **Table 2. Pinning information**

Symbol	Description	Simplified outline	Graphic symbol
S	source	9 7 6 5	D
G	gate		
D	drain		G
D	mounting base; connected to drain	1 2 3 4	sym300 S
	S G D	S source G gate D drain D mounting base; connected to	S source G gate D drain D mounting base; connected to drain

# 6. Ordering information

**Table 3. Ordering information** 

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WMS30N060V	PDFN5X6	WMS30N060VJ	Reel	4000	PDFN5X6N	21-Jul-2022

# 7. Marking

## Table 4. Marking codes

Type number	Marking codes
WMS30N060V	WMS 30N060

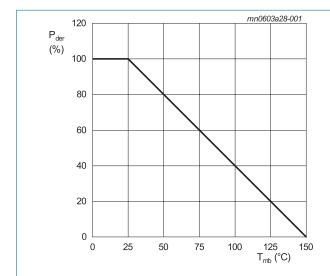
# 8. Limiting values

## **Table 5. Limiting values**

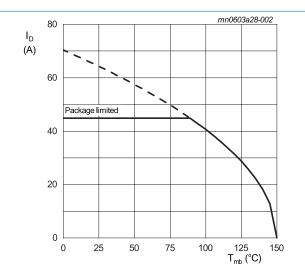
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
$V_{DS}$	drain-source voltage			30	V
$V_{GS}$	gate-source voltage			±20	V
I <sub>D</sub>	continuous drain current	V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 25 °C	[1]	45	Α
		V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 120 °C		32	Α
I <sub>DM</sub>	pulsed drain current	t <sub>p</sub> = 10 μs; T <sub>mb</sub> = 25 °C		180	Α
P <sub>tot</sub>	power dissipation	T <sub>mb</sub> = 25 °C		42	W
E <sub>as</sub>	single pulse drain-to- source avalanche	$I_{AS}$ = 27 A; L = 0.1 mH; $R_{GS}$ = 25 $\Omega$ ; $V_{GS}$ = 10 V; $T_{J}$ = 25 °C		36.5	mJ
T <sub>stg</sub>	storage temperature			-55 to 150	°C
T <sub>j</sub>	junction temperature			-55 to 150	°C

[1] Continuous current is limited by package.



P<sub>der</sub> = (P<sub>tot</sub> / P<sub>tot(25 °C)</sub>) x 100% Fig. 1. Normalized total power dissipation as a function of mounting base temperature



V<sub>GS</sub> = 10 V
Fig. 2. Continuous Drain Current as a function of mounting base temperature

# 9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base			-	2.3	3	K/W
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air	[2]	-	-	55	K/W

[2] Surface mount on FR4 board of 1 inch2, 1 oz copper.

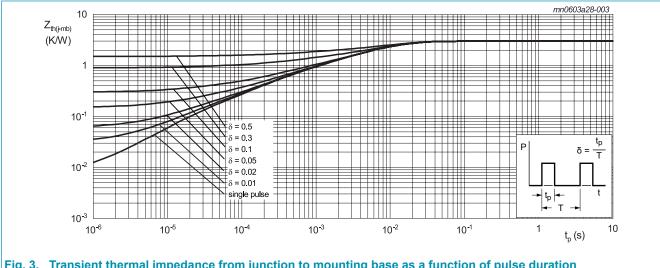


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

# 10. Characteristics

## **Table 7. Characteristics**

T<sub>i</sub> = 25 °C unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V		30	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold voltage	$I_D = 250 \ \mu A; \ V_{DS} = V_{GS}$		1.0	1.7	2.4	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}$		-	-	1	μΑ
		$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125 \text{ °C}$		-	-	10	μΑ
I <sub>GSS</sub>	gate leakage current	$V_{GS} = \pm 20 \text{ V}; V_{DS} = 0 \text{ V}$		-	-	±100	nA
R <sub>DS(on)</sub>	drain-source on-state	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 20 A		-	4.5	6.0	mΩ
	resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 20 A		-	6.6	9.0	mΩ
$R_{G}$	gate resistance	f = 1 MHz		-	2.1	-	Ω
Dynamic	characteristics						
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 20 A; V <sub>DS</sub> = 15 V; V <sub>GS</sub> = 10 V		-	40	-	nC
Q <sub>GS</sub>	gate-source charge			-	6.8	-	nC
$Q_{GD}$	gate-drain charge			-	8.4	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 15 V; V <sub>GS</sub> = 0 V; f = 1 MHz		-	2107	-	pF
C <sub>oss</sub>	output capacitance			-	282	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	229	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = 15 \text{ V}; V_{GS} = 10 \text{ V}; R_G = 6 \Omega;$		-	6.5	-	ns
t <sub>r</sub>	rise time	$I_D = 20 \text{ A}$		-	19	-	ns
$t_{\text{d(off)}}$	turn-off delay time			-	25	-	ns
t <sub>f</sub>	fall time			-	21	-	ns
Source-d	rain diode		-			1	
V <sub>SD</sub>	source-drain voltage	V <sub>GS</sub> = 0 V; I <sub>S</sub> = 1 A		-	0.71	1	V
		V <sub>GS</sub> = 0 V; I <sub>S</sub> = 1 A; T <sub>j</sub> = 125 °C		-	0.54	-	V
Is	body-diode continuous current	T <sub>mb</sub> = 25 °C	[1]	-	-	45	А
t <sub>rr</sub>	reverse recovery time	V <sub>GS</sub> = 0 V; I <sub>S</sub> = 20 A; di/dt = 100 A/μs		-	15	-	ns
Q <sub>rr</sub>	reverse recovered charge			-	8.1	-	nC
I <sub>rrm</sub>	reverse recovery current			-	1.1	-	Α

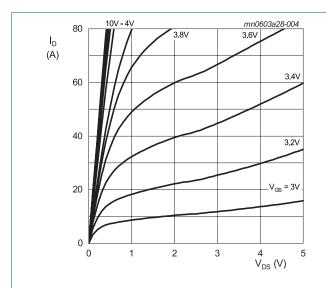
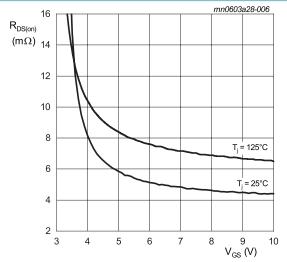


Fig. 4. Drain current as a function of drain-source voltage; typical values



I<sub>D</sub> = 20 A
Fig. 6. Drain-source on-state resistance as a function of gate-source voltage; typical values

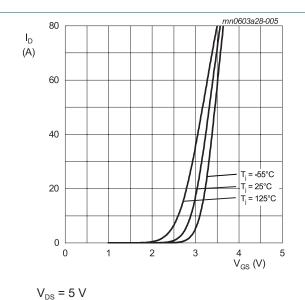


Fig. 5. Drain current as a function of gate-source voltage; typical values

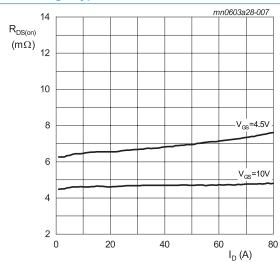
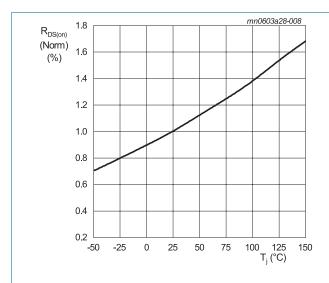
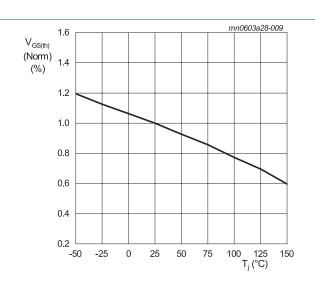


Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



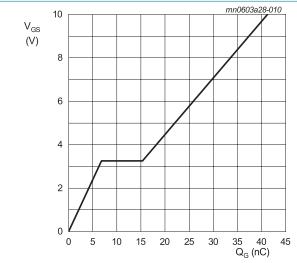
 $V_{GS}$  = 10 V;  $I_{D}$  = 20 A

Fig. 8. Normalized drain-source on-state resistance as a function of junction temperature

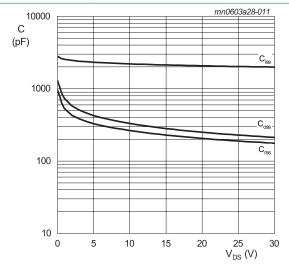


 $V_{GS} = V_{GS}$ ;  $I_D = 250 \mu A$ 

Fig. 9. Normalized gate-source threshold voltage as a function of junction temperature



I<sub>D</sub> = 20 A; V<sub>DS</sub> = 15 V Fig. 10. Gate-source voltage as a function of gate charge; typical values

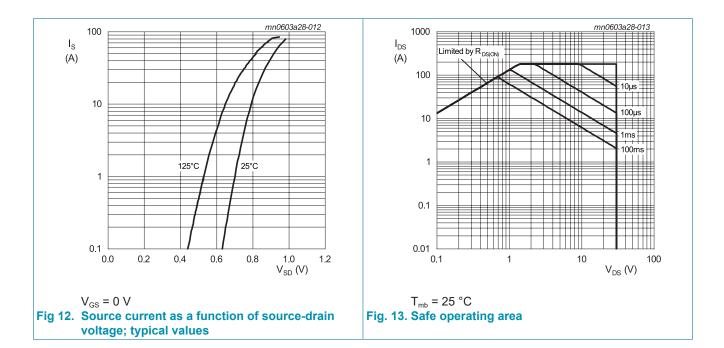


 $V_{GS} = 0 V$ ; f = 1 MHz

Fig 11. Capacitances as a function of drain-source voltage; typical values

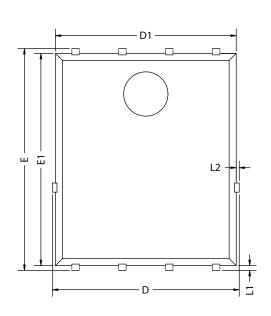
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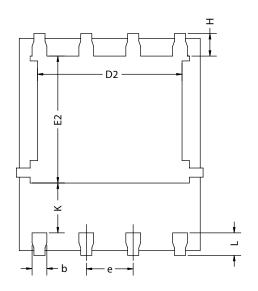
## **N-Channel Silicon MOSFET**

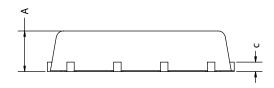


# 11. Package outline









Uni	t	А	Ь	С	D	D1	D2	E	E1	E2	е	Ι	К	L	L1	L2
	min	1.00	0.35	0.21		4.80	3.91	5.90	5.70	3.34	1 27	0.51	1.10	0.51	0.06	
ММ	max	1.20	0.45	0.34	5.10	5.00	4.11	6.10	5.80	3.54	(BŚĆ)	0.71		0.71	0.20	0.10

All dimensions don't include mold flash and metal protrusion.

# 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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# 13. Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information	2
6. Ordering information	2
7. Ordering information	2
8. Limiting values	3
9. Thermal & Mechanical characteristics	4
10. Characteristics	5
11. Package outline	9
12. Legal information	10
13 Contents	12

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